

<b>Notice of References Cited</b>	Application/Control No. 09/892,872		Applicant(s)/Patent Under Reexamination SHIMODA ET AL.	
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**U.S. PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Brown et al, "Nonvolatile Semiconductor Memory Technology: A Comprehensive Guide to Understanding Using NVSM Devices," 1998, IEEE Press, pp. 464-465.
	V	Messenger et al., "FERROELECTRIC MEMORIES: A POSSIBLE ANSWER TO THE HARDENED NONVOLATILE QUESTION," IEEE Transactions on Nuclear Sciences, Vol. 35, No. 6, December 1988, pp. 1461-1466.
★	W	Correspondence from the JPO to Applicant, mailed July 20, 2004.
	X	English translation of correspondence from the JPO to Applicant, mailed July 20, 2004.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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	V	English translation of JP 8-167703A.
	W	English translation of JP 4-364063A.
	X	English translation of JP 9-107067A.

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